# NSN 5962-01-182-6520

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# Body Length:

0.985 inches

#### **Body Width:**

0.280 inches

## **Body Height:**

0.215 inches

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and burn in and bipolar and w/enable and 3-state output

#### Inclosure Material:

Ceramic

#### Inclosure Configuration:

Dual-in-line

## **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

10 input

# **Terminal Surface Treatment:**

Solder

#### Voltage Rating And Type Per Characteristic:

7.0 volts power source

#### Time Rating Per Chacteristic:

75.00 nanoseconds propagation delay time, low to high level output and 75.00 nanoseconds propagation delay time, high to low level output

#### Memory Device Type:

Rom

#### Memory Capacity:

Unknown

#### Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

#### Terminal Type And Quantity:

20 printed circuit

#### Shelf Life:

N/a

## Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

#### Fiig:

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