# NSN 5962-01-183-6570

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# **Body Length:**

Between 0.365 inches and 0.385 inches

# **Body Width:**

Between 0.240 inches and 0.260 inches

# **Body Height:**

0.070 inches

## **Maximum Power Dissipation Rating:**

850.0 milliwatts

## **Operating Tempurature Range:**

-0.0/+85.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Hermetically sealed and monolithic and positive outputs

#### Inclosure Material:

Ceramic

#### Inclosure Configuration:

Flat pack

#### **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

2 input

## **Design Function And Quantity:**

1 gate

## **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

5.5 volts power source

## **Time Rating Per Chacteristic:**

100.00 nanoseconds propagation delay time, low to high level output and 100.00 nanoseconds propagation delay time, high to low level output

## **Test Data Document:**

56232-1603154 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

## Terminal Type And Quantity:

14 flat leads

#### Shelf Life:

N/a

# Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

# Fiig:

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