## NSN 5962-01-185-9604

Memory Microcircuit - Page 1 of 2



| View Online at https://aerobasegroup.com/nsn/5962-01-185-9604  |
|--|
| Body Length:   |
| 0.840 inches   |
| Body Width:  |
| Between 0.220 inches and 0.310 inches  |
| Body Height:   |
| 0.185 inches   |
| Maximum Power Dissipation Rating:  |
| 413.0 milliwatts   |
| Operating Tempurature Range:   |
| -55.0/+125.0 degrees celsius   |
| Storage Tempurature Range:   |
| -65.0/+165.0 degrees celsius   |
| Features Provided:   |
| Hermetically sealed and monolithic and 3-state output  |
| Inclosure Material:  |
| Ceramic and glass  |
| Inclosure Configuration:   |
| Dual-in-line   |
| Output Logic Form:   |
| Transistor-transistor logic  |
| Input Circuit Pattern:   |
| 13 input   |
| Case Outline Source And Designator:  |
| D-2 mil-m-38510  |
| Terminal Surface Treatment:  |
| Solder   |
| Voltage Rating And Type Per Characteristic:  |
| 5.5 volts power source   |
| Time Rating Per Chacteristic:  |
| 50.00 nanoseconds propagation delay time, low to high level output   |
| Memory Device Type:  |
| Ram  |
| Memory Capacity:   |
| Unknown  |
| Test Data Document:  |
| 81349-mil-m-38510 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification |
| format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain          |
| environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).                            |
| Terminal Type And Quantity:  |
| 16 printed circuit   |

Shelf Life:

**Specification Data:** 

81349-mil-m-38510/231 government specification

N/a

## NSN 5962-01-185-9604

Memory Microcircuit - Page 2 of 2



|  | Of |  |  |  |
|--|----|--|--|--|

--

Demilitarization:

Yes - demil/mli

Fiig:

A458a0