# NSN 5962-01-186-4190

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### **Body Length:**

Between 0.245 inches and 0.260 inches

### Body Width:

Between 0.245 inches and 0.260 inches

### **Body Height:**

0.100 inches

### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

### **Features Provided:**

Electrostatic sensitive and hermetically sealed and burn in and schottky

#### Inclosure Material:

Ceramic

#### **Inclosure Configuration:**

Leadless flat pack

## Output Logic Form:

Transistor-transistor logic

### Input Circuit Pattern:

Quad 2 input

### **Design Function And Quantity:**

4 gate, and

### Voltage Rating And Type Per Characteristic:

7.0 volts total supply

### Time Rating Per Chacteristic:

7.00 nanoseconds propagation delay time, low to high level output and 7.50 nanoseconds propagation delay time, high to low level output

### **Test Data Document:**

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

### Terminal Type And Quantity:

16 leadless

### Shelf Life:

N/a

### Unit Of Measure:

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#### Demilitarization:

No

### Fiig:

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