NSN 5962-01-189-5323

Memory Microcircuit - Page 1 of 2



View Online at https://aerobasegroup.com/nsn/5962-01-189-5323

Body Length:
Between 1.180 inches and 1.220 inches
Body Width:
Between 0.580 inches and 0.610 inches
Body Height:
Between 0.080 inches and 0.120 inches
Operating Tempurature Range:
55.0/+125.0 degrees celsius
Storage Tempurature Range:
65.0/+150.0 degrees celsius
End Application:
A/f-18-dcn
Features Provided:
Hermetically sealed and programmable and bipolar and w/enable and 3-state output
nclosure Material:
Ceramic
nclosure Configuration:
Dual-in-line
Output Logic Form:
Fransistor-transistor logic
nput Circuit Pattern:
14 input
Ferminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Fime Rating Per Chacteristic:
50.00 nanoseconds propagation delay time, low to high level output and 50.00 nanoseconds propagation delay time, high to low level
putput
Memory Device Type:
Rom
Memory Capacity:
Jnknown
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Ferminal Type And Quantity:
24 printed circuit
Shelf Life:
N/a
Jnit Of Measure:
-

Yes - demil/mli

Demilitarization:

NSN 5962-01-189-5323

Memory Microcircuit - Page 2 of 2



Fiig:

A458a0