# NSN 5962-01-192-9054

Digital Microcircuit - Page 1 of 1



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## **Body Length:**

0.785 inches

# **Body Width:**

Between 0.220 inches and 0.280 inches

# **Body Height:**

0.180 inches

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Electrostatic sensitive

## **Inclosure Material:**

Ceramic

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

4 wide 4-2-3-2 input

# **Design Function And Quantity:**

1 gate, and-or invert

# **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

7.0 volts power source

## **Time Rating Per Chacteristic:**

7.50 nanoseconds propagation delay time, low to high level output and 7.00 nanoseconds propagation delay time, high to low level output

## Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

## Terminal Type And Quantity:

14 printed circuit

#### Shelf Life:

N/a

#### Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

Fiig:

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