# NSN 5962-01-193-5808

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# **Body Length:**

Between 0.745 inches and 0.785 inches

# Body Width:

Between 0.240 inches and 0.310 inches

# **Body Height:**

Between 0.115 inches and 0.140 inches

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Hermetically sealed and low power and schottky and high speed

### Inclosure Material:

Ceramic

### Inclosure Configuration:

Dual-in-line

### **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

6 input

## **Design Function And Quantity:**

4 register, d-type

# **Terminal Surface Treatment:**

Solder

# Voltage Rating And Type Per Characteristic:

7.0 volts power source

# Time Rating Per Chacteristic:

45.00 nanoseconds propagation delay time, low to high level output and 45.00 nanoseconds propagation delay time, high to low level output

### **Test Data Document:**

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

### Terminal Type And Quantity:

16 printed circuit

### Shelf Life:

N/a

# Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

## Fiig:

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