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Body Length:

0.350 inches

Body Width:

0.265 inches

Body Height:

Between 0.050 inches and 0.080 inches

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Electrostatic sensitive and low power and schottky

Inclosure Material:

Ceramic

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Quad 2 input

Design Function And Quantity:

4 gate, nor

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

7.0 volts power source

Time Rating Per Chacteristic:

12.00 nanoseconds propagation delay time, low to high level output and 16.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Terminal Type And Quantity:

14 flat leads

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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