# NSN 5962-01-193-6024

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# **Body Length:**

Between 0.342 inches and 0.358 inches

# **Body Width:**

Between 0.342 inches and 0.358 inches

# **Body Height:**

Between 0.064 inches and 0.100 inches

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Electrostatic sensitive and low power and schottky

## **Inclosure Material:**

Ceramic

## Inclosure Configuration:

Flat pack

## **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

Triple 3 input

# **Design Function And Quantity:**

3 gate, nand

# Case Outline Source And Designator:

C-2 mil-m-38510

## **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

7.0 volts power source

## Time Rating Per Chacteristic:

10.00 nanoseconds propagation delay time, low to high level output and 17.00 nanoseconds propagation delay time, high to low level output

## Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

## Terminal Type And Quantity:

20 flat leads

#### Shelf Life:

N/a

# Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

#### Fiig:

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