NSN 5962-01-194-4209

Memory Microcircuit - Page 1 of 1



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Body Length:
0.785 inches
Body Width:
Between 0.295 inches and 0.305 inches
Body Height:
0.170 inches
Maximum Power Dissipation Rating:
1.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Burn in and compatible cmos and 3-state output
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
13 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Memory Device Type:
Rom
Test Data Document:
30003-704as5566 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,
etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
16 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fila:

Fiig:

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