NSN 5962-01-195-1426

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Body Length:

- 0.990 inches
- **Body Width:**
- 0.300 inches

Body Height:

0.175 inches

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and burn in and monolithic and positive outputs

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

12 input

Design Function And Quantity:

6 gate, array

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 7.0 volts power source

Time Rating Per Chacteristic:

45.00 nanoseconds propagation delay time, low to high level output and 45.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Terminal Type And Quantity:

20 printed circuit

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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