NSN 5962-01-195-4645

Digital Microcircuit - Page 1 of 1



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Maximum Power Dissipation Rating:

400.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Electrostatic sensitive and burn in and high speed

Inclosure Material:

Ceramic

Inclosure Configuration:

Flat pack

Output Logic Form:

Complementary-metal oxide-semiconductor logic

Input Circuit Pattern:

Triple 3 input

Design Function And Quantity:

3 gate, nor

Case Outline Source And Designator:

F-1 mil-m-38510 or f-2 mil-m-38510 or f-3 mil-m-38510

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-0.5 volts power source and 18.0 volts power source

Time Rating Per Chacteristic:

250.00 nanoseconds propagation delay time, low to high level output and 250.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

06481-974083 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

14 flat leads

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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