

View Online at <https://aerobasegroup.com/nsn/5962-01-197-2745>

Features Provided:

Bipolar and burn in and programmable and schottky and 3-state output and w/enable

Inclosure Material:

Ceramic

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

14 input

Terminal Surface Treatment:

Solder

Memory Device Type:

Rom

Test Data Document:

88818-a574a777 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

24 flat leads

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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