NSN 5962-01-198-7102

Digital Microcircuit - Page 1 of 1



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Body Length:
Between 0.490 inches and 0.520 inches
Body Width:
Between 0.245 inches and 0.285 inches
Body Height:
Between 0.045 inches and 0.085 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic and low power and schottky and 3-state output
Inclosure Material:
Ceramic
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
10 input
Design Function And Quantity:
1 decoder, one of eight line
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-0.5 volts power source and 7.0 volts power source
Time Rating Per Chacteristic:
45.00 nanoseconds propagation delay time, low to high level output and 52.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
94117-313122 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format;
excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental
and performance requirements and test conditions that are shown as "typical", "average", "", etc.).
Terminal Type And Quantity:
20 flat leads
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli

Fiig:

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