NSN 5962-01-199-0058

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Body Length:
0.390 inches
Body Width:
Between 0.235 inches and 0.260 inches
Body Height:
Between 0.045 inches and 0.085 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic and hermetically sealed and w/totem pole output
Inclosure Material:
Ceramic
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Quad 2 input
Design Function And Quantity:
4 gate, and
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-0.5 volts power source and 7.0 volts power source
Time Rating Per Chacteristic:
7.50 nanoseconds propagation delay time, low to high level output and 7.50 nanoseconds propagation delay time, high to low level output
Test Data Document:
94117-310489 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format;
excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental
and performance requirements and test conditions that are shown as "typical", "average", "", etc.).
Terminal Type And Quantity:
14 flat leads
Shelf Life:
N/a
Unit Of Measure:

Demilitarization:
Yes - demil/mli
Fiig:
A458a0