

View Online at <https://aerobasegroup.com/nsn/5962-01-199-2578>

Features Provided:

Monolithic and burn in and programmable

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

10 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

5.5 volts power source

Time Rating Per Characteristic:

90.00 nanoseconds propagation delay time, low to high level output and 90.00 nanoseconds propagation delay time, high to low level output

Memory Device Type:

Rom

Test Data Document:

82577-3567351-3 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

20 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

No

Fig:

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