# NSN 5962-01-199-9161

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# Body Length: 1.290 inches Body Width: Between 0.500 inches and 0.610 inches Body Height: 0.210 inches Maximum Power Dissipation Rating: 855.0 milliwatts Operating Tempurature Range: -55.0/+125.0 degrees celsius Storage Tempurature Range:

-65.0/+125.0 degrees celsius

#### **End Application:**

Receiving set, countermeasures an/alr-56c

#### **Features Provided:**

Monolithic and erasable and programmable and electrostatic sensitive and w/enable and bidirectional

#### Inclosure Material:

Ceramic

### Inclosure Configuration:

Dual-in-line

#### Output Logic Form:

N-type metal oxide-semiconductor logic

#### **Input Circuit Pattern:**

21 input

#### **Case Outline Source And Designator:**

D-3 mil-m-38510

#### **Terminal Surface Treatment:**

Solder

#### Voltage Rating And Type Per Characteristic:

-0.3 volts power source and 6.0 volts power source

#### **Time Rating Per Chacteristic:**

300.00 nanoseconds propagation delay time, low to high level output and 300.00 nanoseconds propagation delay time, high to low level output

## Memory Device Type:

Rom

#### **Memory Capacity:**

Unknown

#### **Test Data Document:**

81413-201842 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

### **Terminal Type And Quantity:**

24 printed circuit

Shelf Life:

N/a

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Unit Of Measure:

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Demilitarization:

Yes - demil/mli

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