NSN 5962-01-199-9166

Digital Microcircuit - Page 1 of 1



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Body Length:

0.840 inches

Body Width:

Between 0.220 inches and 0.310 inches

Body Height:

Between 0.140 inches and 0.185 inches

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Monolithic and low power and schottky and positive edge triggered and w/clock and w/clear and w/preset and electrostatic sensitive

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Dual 4 input

Design Function And Quantity:

2 flip-flop, j-k

Case Outline Source And Designator:

D-2 mil-m-38510

Time Rating Per Chacteristic:

18.00 nanoseconds propagation delay time, low to high level output and 20.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

81413-201921 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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