## NSN 5962-01-199-9398

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Operating Tempurature Range:
-55.0/+125.0 degrees celsius
End Application:
1430-01-087-6330
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
9 input
Design Function And Quantity:
3 adder, full, binary
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.25 volts power source
Test Data Document:
18876-10271754 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,
etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
14 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
A458a0