NSN 5962-01-200-9718

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Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Electrostatic sensitive and hermetically sealed and monolithic and positive outputs
Inclosure Material:
Silicon
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Quad 2 input
Design Function And Quantity:
4 buffer, nand
Terminal Surface Treatment:
Solder
Terminal Type And Quantity:
14 flat leads
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
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