NSN 5962-01-201-4519

Memory Microcircuit - Page 1 of 1



View Online at https://aerobasegroup.com/nsn/5962-01-201-4519 **Body Length:** Between 0.880 inches and 0.900 inches **Body Width:** Between 0.285 inches and 0.300 inches **Body Height:** Between 0.145 inches and 0.170 inches **Operating Tempurature Range:** -55.0/+125.0 degrees celsius **Storage Tempurature Range:** -65.0/+150.0 degrees celsius **Features Provided:** 3-state output and wire-or outputs and schottky and programmed and bipolar **Inclosure Material:** Ceramic **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 12 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

7.0 volts power source

Memory Device Type:

Rom

Test Data Document:

05869-726017-2 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

18 printed circuit

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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