# NSN 5962-01-202-2414

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# **Body Length:**

Between 1.230 inches and 1.285 inches

## **Body Width:**

Between 0.245 inches and 0.285 inches

# **Body Height:**

Between 0.125 inches and 0.160 inches

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

3-state output and schottky and low power and w/enable

#### **Inclosure Material:**

Ceramic

#### Inclosure Configuration:

Dual-in-line

## **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

13 input

**Design Function And Quantity:** 

1 buffer

**Terminal Surface Treatment:** 

Solder

## Voltage Rating And Type Per Characteristic:

-0.5 volts power source and 7.0 volts power source

## **Time Rating Per Chacteristic:**

28.00 nanoseconds propagation delay time, low to high level output and 28.00 nanoseconds propagation delay time, high to low level output

## **Test Data Document:**

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

## Terminal Type And Quantity:

24 printed circuit

#### Shelf Life:

N/a

#### Unit Of Measure:

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## Demilitarization:

No

Fiig:

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