NSN 5962-01-202-2961

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Body Length:	
0.840 inches	
Body Width:	
Between 0.220 inche	es and 0.310 inches
Body Height:	
0.185 inches	
Maximum Power Di	ssipation Rating:
739.0 milliwatts	
Operating Tempura	ture Range:
-55.0/+125.0 degrees	
Storage Tempuratu	
-65.0/+150.0 degrees	
Features Provided:	
Hermetically sealed a	and programmable and bipolar and schottky
Inclosure Material:	
Ceramic	
Inclosure Configura	ation:
Dual-in-line	
Output Logic Form:	
Transistor-transistor	
Input Circuit Patteri	n:
10 input	
Case Outline Sourc	e And Designator:
D-2 mil-m-38510	
Terminal Surface T	reatment:
Solder	
Voltage Rating And	Type Per Characteristic:
7.0 volts power sour	ce
Time Rating Per Ch	acteristic:
75.00 nanoseconds	propagation delay time, low to high level output and 75.00 nanoseconds propagation delay time, high to low level
output	
Memory Device Typ	De:
Rom	
Memory Capacity:	
Unknown	
Test Data Documen	it:
96906-mil-std-883 st	andard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And	Quantity:
16 printed circuit	
Shelf Life:	
N/a	
Unit Of Measure:	

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Demilitarization:

Yes - demil/mli

Fiig:

A458a0

