NSN 5962-01-202-8620

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Between 0.371 inches and 0.400 inches

Body Width:

Between 0.247 inches and 0.285 inches

Body Height:

Between 0.050 inches and 0.080 inches

Maximum Power Dissipation Rating:

40.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Bipolar and burn in, mil-std-883, class b and complementary outputs and dynamic and hermetically sealed and monolithic and positive edge triggered and positive outputs and schottky and w/common clock and w/common enable and w/flat pack carrier and w/independent outputs and w/totem pole output and 2-state output

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

4 stage and 6 input

Design Function And Quantity:

4 flip-flop, d-type, clocked

Case Outline Source And Designator:

Mo-004ag joint electron device engineering council

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

7.0 volts power source

Time Rating Per Chacteristic:

27.00 nanoseconds propagation delay time, low to high level output and 27.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

96906-mil-std-833 standard (includes industry or association standards, individual manufactureer standards, etc.).

Terminal Type And Quantity:

16 flat leads

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Vec - demil/mli

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