# NSN 5962-01-203-5885

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## **Overall Height:**

0.400 inches

## **Body Length:**

0.785 inches

## Body Width:

Between 0.220 inches and 0.310 inches

#### **Body Height:**

Between 0.140 inches and 0.185 inches

## **Maximum Power Dissipation Rating:**

200.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and burn in and monolithic and positive outputs

#### Inclosure Material:

Ceramic

#### Inclosure Configuration:

Dual-in-line

## Output Logic Form:

Complementary-metal oxide-semiconductor logic

#### Input Circuit Pattern:

Quad 2 input

## **Design Function And Quantity:**

4 gate, exclusive or

#### **Case Outline Source And Designator:**

D-1 mil-m-38510

#### **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

18.0 volts power source

## **Time Rating Per Chacteristic:**

350.00 nanoseconds propagation delay time, low to high level output and 350.00 nanoseconds propagation delay time, high to low level output

## Test Data Document:

81349-mil-m-38510 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).

# Terminal Type And Quantity:

14 printed circuit

## Specification Data:

81349-mil-m-38510/53 government specification

# Shelf Life:

NI/o

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Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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