## NSN 5962-01-203-6149

Memory Microcircuit - Page 1 of 1



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Features Provided:	
Bipolar and 3-state output	
Output Logic Form:	
Transistor-transistor logic	
Input Circuit Pattern:	
12 input	
Terminal Surface Treatment:	
Solder	
Memory Device Type:	
Rom	
Shelf Life:	
N/a	
Unit Of Measure:	
Demilitarization:	
Yes - demil/mli	
Fiig:	
A458a0	