NSN 5962-01-203-9746

Memory Microcircuit - Page 1 of 2



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Body Length:	
0.840 inches	
Body Width:	
Between 0.220 inch	es and 0.310 inches
Body Height:	
0.185 inches	
Maximum Power D	issipation Rating:
794.0 milliwatts	
Operating Tempur	ature Range:
-55.0/+125.0 degree	es celsius
Storage Tempurat	ire Range:
-65.0/+150.0 degree	es celsius
Features Provided	
3-state output and b	ipolar and programmed and schottky and w/active pull-up
Inclosure Material:	
Ceramic or glass or	metal
Inclosure Configu	ation:
Dual-in-line	
Output Logic Form	:
Transistor-transistor	logic
Input Circuit Patte	n:
10 input	
Case Outline Sour	ce And Designator:
D-2 mil-m-38510	
Terminal Surface	reatment:
Solder	
Voltage Rating An	d Type Per Characteristic:
-0.5 volts power so	urce and 7.0 volts power source
Time Rating Per C	nacteristic:
85.00 nanoseconds	propagation delay time, low to high level output and 85.00 nanoseconds propagation delay time, high to low level
output	
Memory Device Ty	pe:
Prom	
Test Data Docume	nt:
96906-mil-std-883 s	tandard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And	Quantity:
16 printed circuit	
Shelf Life:	
N/a	
Unit Of Measure:	
 Demilitarization:	
Yes - demil/mli	

NSN 5962-01-203-9746

Memory Microcircuit - Page 2 of 2

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