# NSN 5962-01-204-1650

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## **Body Length:**

Between 0.755 inches and 0.785 inches

## Body Width:

Between 0.245 inches and 0.271 inches

# **Body Height:**

0.185 inches

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **End Application:**

Computer-display set an/uyq-2 (v)

## **Features Provided:**

Schottky and programmed and high speed and w/open collector

#### Inclosure Material:

Ceramic

## Inclosure Configuration:

Dual-in-line

## **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

10 input

#### **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

7.0 volts power source

## **Time Rating Per Chacteristic:**

60.00 nanoseconds propagation delay time, low to high level output and 60.00 nanoseconds propagation delay time, high to low level output

#### Memory Device Type:

Rom

#### Memory Capacity:

Unknown

#### Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

#### **Terminal Type And Quantity:**

16 printed circuit

#### Shelf Life:

N/a

#### Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

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