NSN 5962-01-204-6381

Memory Microcircuit - Page 1 of 2



View Online at https://aerobasegroup.com/nsn/5962-01-204-6381

Body Length:

Between 0.755 inches and 0.785 inches

Body Width:

Between 0.265 inches and 0.291 inches

Body Height:

Between 0.150 inches and 0.199 inches

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

End Application:

Computer-display set an/uyq-2 (v)

Features Provided:

Bipolar and high speed and programmed

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

10 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

7.0 volts power source

Time Rating Per Chacteristic:

60.00 nanoseconds propagation delay time, low to high level output and 60.00 nanoseconds propagation delay time, high to low level output

Memory Device Type:

Rom

Memory Capacity:

Unknown

Test Data Document:

05869-726015-2 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;

excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

16 printed circuit

Shelf Life:

N/a

Unit Of Measure:

_

Demilitarization:

Yes - demil/mli

NSN 5962-01-204-6381

Memory Microcircuit - Page 2 of 2

Fiig: A458a0

