NSN 5962-01-210-1828

Memory Microcircuit - Page 1 of 1



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Body Length:
Between 0.890 inches and 0.930 inches
Body Width:
Between 0.280 inches and 0.310 inches
Body Height:
Between 0.080 inches and 0.310 inches
Operating Tempurature Range:
-55.0 to 125.0 degrees celsius
Storage Tempurature Range:
-65.0 to 150.0 degrees celsius
End Application:
5805-01-032-1694 switchboard, tel
Features Provided:
Programmable and hermetically sealed and monolithic and schottky
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
12 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Time Rating Per Chacteristic:
125.00 nanoseconds propagation delay time, low to high level output and 125.00 nanoseconds propagation delay time, high to low level
output
Memory Device Type:
Rom
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
18 printed circuit
Shelf Life:
N/a
Unit Of Measure:

Demilitarization:
Yes - demil/mli

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