NSN 5962-01-211-9981

Memory Microcircuit - Page 1 of 1



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Body Length:	
0.975 inches	
Body Width:	
0.300 inches	
Body Height:	
0.170 inches	
Operating Tem	purature Range:
-55.0 to 125.0 d	egrees celsius
Storage Tempu	rature Range:
-65.0 to 150.0 d	egrees celsius
Features Provi	ded:
Programmable a	and schottky and low voltage
Inclosure Mate	rial:
Ceramic	
Inclosure Conf	guration:
Dual-in-line	
Output Logic F	orm:
Transistor-trans	stor logic
Input Circuit Pa	attern:
11 input	
Bit Quantity (ne	on-core):
4096	
Word Quantity	(non-core):
512	
Terminal Surfa	ce Treatment:
Solder	
Voltage Rating	And Type Per Characteristic:
7.0 volts power	source
Memory Device	э Туре:
Rom	
Test Data Docu	ment:
96906-mil-std-8	33 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type	And Quantity:
20 printed circui	t in the second s
Shelf Life:	
N/a	
Unit Of Measur	e:
Demilitarizatio	1:
Yes - demil/mli	
Fiig:	
A458a0	