NSN 5962-01-214-2509

Memory Microcircuit - Page 1 of 2



propagation delay time, high to low level

View Online at https://aerobasegroup.com/nsn/5962-01-214-2509

Body Length:	
0.840 inches	
Body Width:	
Between 0.220 inches and 0.310 inches	
Body Height:	
0.185 inches	
Maximum Power Dissipation Rating:	
739.0 milliwatts	
Operating Tempurature Range:	
-55.0/+125.0 degrees celsius	
Storage Tempurature Range:	
-65.0/+150.0 degrees celsius	
End Application:	
Receiving set, countermeasures an/slr-16a	
Features Provided:	
Programmable and bipolar and schottky and 3-state output	
Inclosure Material:	
Ceramic and glass	
Inclosure Configuration:	
Dual-in-line	
Output Logic Form:	
Transistor-transistor logic	
Input Circuit Pattern:	
6 input	
Case Outline Source And Designator:	
D-2 mil-m-38510	
Terminal Surface Treatment:	
Solder	
Voltage Rating And Type Per Characteristic:	
-0.5 volts power source and 7.0 volts power source	
Time Rating Per Chacteristic:	
80.00 nanoseconds propagation delay time, low to high level output and 80.00 nan	oseconds propagation delay tim
output	
Memory Device Type:	
Rom	
Test Data Document:	
96906-mil-std-883 standard (includes industry or association standards, individual n	manufactureer standards, etc.).
Terminal Type And Quantity:	
16 printed circuit	
Shelf Life:	
N/a	
Unit Of Measure:	

NSN 5962-01-214-2509

Memory Microcircuit - Page 2 of 2

Demilitarization:

Yes - demil/mli

Fiig:

A458a0

