NSN 5962-01-214-4456

Memory Microcircuit - Page 1 of 1



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Body Length:
Between 1.050 inches and 1.110 inches
Body Width:
Between 0.360 inches and 0.410 inches
Body Height:
Between 0.080 inches and 0.140 inches
Operating Tempurature Range:
-55.0 to 125.0 degrees celsius
Storage Tempurature Range:
-65.0 to 150.0 degrees celsius
End Application:
Radar set an/fps-115
Features Provided:
W/enable and bipolar and high impedance and asynchronous and synchronous and edge triggered
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
12 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-0.5 volts power source and 7.0 volts power source
Time Rating Per Chacteristic:
45.00 nanoseconds propagation delay time, low to high level output and 45.00 nanoseconds propagation delay time, high to low level
output
Memory Device Type:
Rom
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
22 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:

Fiig:

A458a0

Yes - demil/mli