## NSN 5962-01-214-8834

Memory Microcircuit - Page 1 of 1



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| Features Provided:   |
|--|
| Bipolar and programmable and w/enable and 3-state output and monolithic  |
| Inclosure Configuration:   |
| Dual-in-line   |
| Output Logic Form:   |
| Transistor-transistor logic  |
| Input Circuit Pattern:   |
| 14 input   |
| Voltage Rating And Type Per Characteristic:  |
| 7.0 volts power source   |
| Memory Device Type:  |
| Prom   |
| Test Data Document:  |
| 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.). |
| Shelf Life:  |
| N/a  |
| Unit Of Measure:   |
|  |
| Demilitarization:  |
| Yes - demil/mli  |
| Fiig:  |
| A458a0   |
|  |
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