NSN 5962-01-215-1741



Memory Microcircuit - Page 1 of 1 View Online at https://aerobasegroup.com/nsn/5962-01-215-1741 **Body Length:** Between 0.880 inches and 0.900 inches **Body Width:** Between 0.285 inches and 0.300 inches **Body Height:** Between 0.145 inches and 0.170 inches **Operating Tempurature Range:** -55.0 to 125.0 degrees celsius Storage Tempurature Range: -65.0 to 150.0 degrees celsius **Features Provided:** Programmable and schottky and high speed and 3-state output **Inclosure Material:** Ceramic **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 12 input **Terminal Surface Treatment:** Solder **Voltage Rating And Type Per Characteristic:** 7.0 volts power source **Time Rating Per Chacteristic:** 70.00 nanoseconds propagation delay time, low to high level output and 70.00 nanoseconds propagation delay time, high to low level output **Memory Device Type:** Rom **Test Data Document:** 49956-1067465 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing) **Terminal Type And Quantity:** 18 printed circuit Shelf Life: N/a **Unit Of Measure:**

Demilitarization:

Yes - demil/mli

Fiig:

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