NSN 5962-01-218-8482

Memory Microcircuit - Page 1 of 1



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Body Length:

Between 0.930 inches and 0.975 inches

Body Width:

Between 0.245 inches and 0.300 inches

Body Height:

0.150 inches

Operating Tempurature Range:

+0.0/+70.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and schottky and 3-state output and programmable

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

10 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

7.0 volts power source

Time Rating Per Chacteristic:

15.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level output

Memory Device Type:

Rom

Terminal Type And Quantity:

20 printed circuit

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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