NSN 5962-01-220-5841

Memory Microcircuit - Page 1 of 1



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0.840 inches	
Body Width:	
Between 0.220 inche	s and 0.310 inches
Body Height:	
0.185 inches	
Maximum Power Di	sipation Rating:
739.0 milliwatts	
Operating Tempura	ure Range:
-55.0/+125.0 degree	celsius
Storage Tempuratu	e Range:
-65.0/+150.0 degree	celsius
Features Provided:	
Schottky and bipolar	and monolithic and programmable and 3-state output and high impedance
Inclosure Configura	tion:
Dual-in-line	
Output Logic Form	
Transistor-transistor	ogic
Input Circuit Patter	
10 input	
Case Outline Sourc	e And Designator:
D-2 mil-m-38510	
Terminal Surface T	eatment:
Solder	
Voltage Rating And	Type Per Characteristic:
7.0 volts power sour	Хе
Time Rating Per Ch	acteristic:
75.00 nanoseconds	propagation delay time, low to high level output and 75.00 nanoseconds propagation delay time, high to low level
output	
Memory Device Typ	e:
Rom	
Test Data Documer	t:
96906-mil-std-883 st	andard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And	Quantity:
16 printed circuit	
Shelf Life:	
N/a	
Unit Of Measure:	
Demilitarization:	
Yes - demil/mli	
Fiig:	
A458a0	