NSN 5962-01-225-3502

Memory Microcircuit - Page 1 of 1



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Body Length:

1.260 inches

Body Width:

0.530 inches

Body Height:

0.170 inches

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

End Application:

An/ky-71a

Features Provided:

Hermetically sealed and bipolar and high speed and 3-state output and w/enable

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

14 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-0.3 volts power source and 7.0 volts power source

Time Rating Per Chacteristic:

Err-060 hand pull and err-060 hand

Memory Device Type:

Rom

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Terminal Type And Quantity:

24 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

A458a0