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End Application:

An/aam-6 (v) test set, electro-optical system

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Quad 2 input

Design Function And Quantity:

4 multiplexer

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fig:

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