## NSN 5962-01-226-3931

Memory Microcircuit - Page 1 of 1



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Body Length:
Between 1.050 inches and 1.110 inches
Body Width:
Between 0.360 inches and 0.405 inches
Body Height:
Between 0.140 inches and 0.175 inches
Operating Tempurature Range:
-55.0 to 125.0 degrees celsius
Storage Tempurature Range:
-65.0 to 150.0 degrees celsius
Features Provided:
Static operation and hermetically sealed and high speed
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Complementary-metal oxide-semiconductor logic
Input Circuit Pattern:
14 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Time Rating Per Chacteristic:
25.00 nanoseconds propagation delay time, low to high level output and 25.00 nanoseconds propagation delay time, high to low level
output
Memory Device Type:
Rom
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
22 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
File:

Fiig:

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