NSN 5962-01-229-8855

Memory Microcircuit - Page 1 of 1



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Body Length:

Between 0.755 inches and 0.785 inches

Body Width:

Between 0.265 inches and 0.291 inches

Body Height:

0.199 inches

Storage Tempurature Range:

-65.0 to 150.0 degrees celsius

Features Provided:

3-state output and bipolar and w/enable and w/buffered output

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

10 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-0.5 volts power source and 7.0 volts power source

Memory Device Type:

Rom

Test Data Document:

27914-51022 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

16 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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