NSN 5962-01-233-3493

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Body Length:
0.785 inches
Body Width:
Between 0.220 inches and 0.310 inches
Body Height:
0.140 inches
Maximum Power Dissipation Rating:
8.2 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Low power and monolithic and schottky
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Dual 4 input
Design Function And Quantity:
2 gate, nand
Case Outline Source And Designator:
D-1 mil-m-38510
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-0.5 volts multiplex input signal and 7.0 volts power source
Time Rating Per Chacteristic:
13.00 nanoseconds propagation delay time, low to high level output and 12.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
81349-mil-m-38510 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification
format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain
environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).
Terminal Type And Quantity:
14 printed circuit
Specification Data:
81349-mil-m-38510/370 government specification
Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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