## NSN 5962-01-235-1343

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## **Body Length:** 0.390 inches **Body Width:** Between 0.235 inches and 0.260 inches **Body Height:** Between 0.045 inches and 0.085 inches **Maximum Power Dissipation Rating:** 49.5 milliwatts **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **Features Provided:** Hermetically sealed and monolithic and schottky **Inclosure Material:** Ceramic and glass **Inclosure Configuration:** Flat pack **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** Dual 4 input **Design Function And Quantity:** 2 gate, nand **Case Outline Source And Designator:** F-2 mil-m-38510 **Terminal Surface Treatment:** Solder Voltage Rating And Type Per Characteristic: 7.0 volts power source **Time Rating Per Chacteristic:** 9.00 nanoseconds propagation delay time, low to high level output and 9.00 nanoseconds propagation delay time, high to low level output **Test Data Document:** 81349-mil-m-38510 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.). **Terminal Type And Quantity:** 14 flat leads **Specification Data:** 81349-mil-m-38510/70 government specification

Shelf Life:

N/a

- Unit Of Measure:
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## Demilitarization:

Yes - demil/mli

Fiig:

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