NSN 5962-01-236-1520

Memory Microcircuit - Page 1 of 1



View Online at https://aerobasegroup.com/nsn/5962-01-236-1520

Padulanath	
Body Length: 0.400 inches	
Body Width:	
0.385 inches	
Body Height:	
0.090 inches	
Maximum Power Dissipation Rating:	
935.0 milliwatts	
Operating Tempurature Range:	
-55.0/+125.0 degrees celsius	
Storage Tempurature Range:	
-65.0/+150.0 degrees celsius	
Features Provided:	
Hermetically sealed and burn in and programmable and schottky and bipolar	
Inclosure Material:	
Ceramic	
Inclosure Configuration:	
Flat pack	
Output Logic Form:	
Transistor-transistor logic	
Input Circuit Pattern:	
13 input	
Terminal Surface Treatment:	
Solder	
Voltage Rating And Type Per Characteristic:	
7.0 volts power source	
Time Rating Per Chacteristic:	
90.00 nanoseconds propagation delay time, low to high level output and 90.00 nanoseconds propagation delay time, high to low level	
output	
Memory Device Type:	
Rom	
Test Data Document:	
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).	
Terminal Type And Quantity:	
24 flat leads	
Shelf Life:	
N/a	
Unit Of Measure:	
 Demilitarization:	
Yes - demil/mli	
Fiig:	
A458a0	