# NSN 5962-01-236-5871

Memory Microcircuit - Page 1 of 1



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# **Body Length:**

0.440 inches

## **Body Width:**

0.275 inches

## **Body Height:**

0.065 inches

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

# **Features Provided:**

W/buffered output and hermetically sealed and bipolar and 3-state output and burn in

## Inclosure Material:

Ceramic

## Inclosure Configuration:

Flat pack

# **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

10 input

**Terminal Surface Treatment:** 

Solder

# Voltage Rating And Type Per Characteristic:

7.0 volts power source

# **Time Rating Per Chacteristic:**

100.00 nanoseconds propagation delay time, low to high level output and 100.00 nanoseconds propagation delay time, high to low level output

#### Memory Device Type:

Rom

# Test Data Document:

88818-a574a244 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,

etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

#### **Terminal Type And Quantity:**

16 flat leads

Shelf Life:

N/a

#### Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

# Fiig:

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