# NSN 5962-01-236-9499

Memory Microcircuit - Page 1 of 1



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# **Body Length:**

Between 0.885 inches and 0.915 inches

# **Body Width:**

Between 0.285 inches and 0.305 inches

# **Body Height:**

Between 0.140 inches and 0.170 inches

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

High speed and bipolar and 3-state output and w/enable and schottky

#### **Inclosure Material:**

Ceramic and glass

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

12 input

**Terminal Surface Treatment:** 

Solder

## Voltage Rating And Type Per Characteristic:

-0.3 volts power source and 7.0 volts power source

## Time Rating Per Chacteristic:

90.00 nanoseconds propagation delay time, low to high level output and 90.00 nanoseconds propagation delay time, high to low level output

## Memory Device Type:

Rom

## Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

## Terminal Type And Quantity:

18 printed circuit

#### Shelf Life:

N/a

# Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

#### Fiig:

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