NSN 5962-01-242-4222

Memory Microcircuit - Page 1 of 1



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Body L	ength:
0.840 in	ches
Body W	/idth:
0.310 in	ches
Body H	eight:
0.185 in	ches
Maximu	Im Power Dissipation Rating:
739.0 m	illiwatts
Operati	ng Tempurature Range:
-55.0/+1	25.0 degrees celsius
Storage	e Tempurature Range:
-65.0/+1	50.0 degrees celsius
Feature	es Provided:
Monolith	nic and programmable and bipolar
Inclosu	re Material:
Ceramic	
Inclosu	re Configuration:
Dual-in-	line
Output	Logic Form:
Transist	or-transistor logic
Input C	ircuit Pattern:
6 input	
Case O	utline Source And Designator:
D-2 mil-	m-38510
Termina	al Surface Treatment:
Solder	
Voltage	Rating And Type Per Characteristic:
-0.5 volt	s power source and 7.0 volts power source
Memory	y Device Type:
Prom	
Test Da	ta Document:
96906-n	nil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Termina	al Type And Quantity:
16 printe	ed circuit
Shelf Li	ife:
N/a	
Unit Of	Measure:
Demilita	arization:
Yes - de	emil/mli
Fiig:	
A458a0	