NSN 5962-01-242-8010

Memory Microcircuit - Page 1 of 1



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Body Length:
Between 0.580 inches and 0.620 inches
Body Width:
Between 0.360 inches and 0.385 inches
Body Height:
Between 0.050 inches and 0.090 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
End Application:
Detecting-ranging set, sonar an/sqs-53b
Features Provided:
Programmable and 3-state output and high speed
Inclosure Material:
Ceramic
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
14 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Time Rating Per Chacteristic:
80.00 nanoseconds propagation delay time, low to high level output and 80.00 nanoseconds propagation delay time, high to low level
output
Memory Device Type:
Rom
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
24 flat leads
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Vas - damil/mli

Fiig:

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