## NSN 5962-01-245-1731

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## **Body Length:** 0.840 inches **Body Width:** Between 0.220 inches and 0.310 inches **Body Height:** 0.140 inches **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **Features Provided:** Positive edge triggered and w/clear and w/clock and low power and schottky **Inclosure Material:** Ceramic and glass **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 8 input **Design Function And Quantity:** 6 flip-flop, d-type **Case Outline Source And Designator:** D-2 mil-m-38510 **Terminal Surface Treatment:** Solder Voltage Rating And Type Per Characteristic: 7.0 volts power source **Time Rating Per Chacteristic:** 30.00 nanoseconds propagation delay time, low to high level output and 35.00 nanoseconds propagation delay time, high to low level output **Test Data Document:** 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.). **Terminal Type And Quantity:** 16 printed circuit Shelf Life: N/a Unit Of Measure: ---**Demilitarization:** Yes - demil/mli Fiig: A458a0