NSN 5962-01-245-3557

Memory Microcircuit - Page 1 of 2



View Online at https://aerobasegroup.com/nsn/5962-01-245-3557

Body Length: 0.900 inches **Body Width:** 0.300 inches **Body Height:** 0.150 inches **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **End Application:** Console, computer display oj-45 (v) 11 **Features Provided:** Hermetically sealed and burn in and programmed and schottky **Inclosure Material:** Ceramic **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 12 input **Terminal Surface Treatment:** Solder Voltage Rating And Type Per Characteristic: 7.0 volts power source **Time Rating Per Chacteristic:** 70.00 nanoseconds propagation delay time, low to high level output and 70.00 nanoseconds propagation delay time, high to low level output Memory Device Type: Rom

--

Memory Capacity:

Unknown

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Terminal Type And Quantity:

18 printed circuit

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

NSN 5962-01-245-3557

Memory Microcircuit - Page 2 of 2

Fiig: A458a0

